

FIG. 1 (Prior Art)

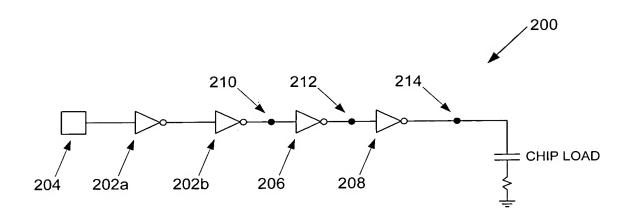


FIG. 2 (Prior Art)

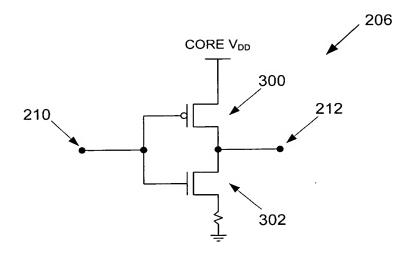


FIG. 3 (Prior Art)

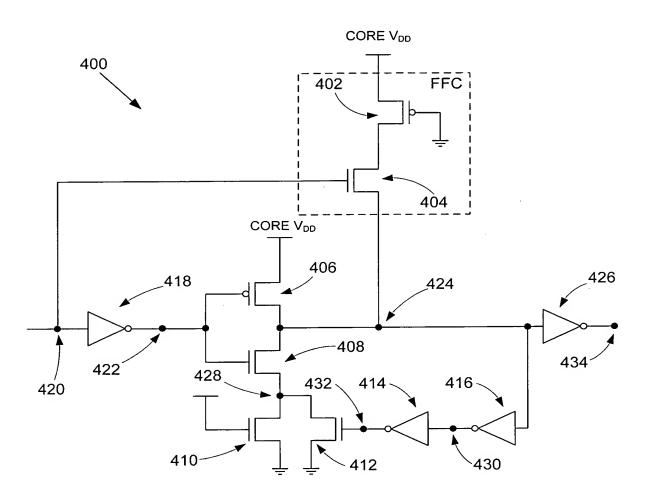


FIG. 4A

Title: FEED-FORWARD CIRCUIT FOR REDUCING DELAY THROUGH AN INPUT BUFFER Inventors: Becker et al.

App. No. 10/759,339 Docket No. ARTCP043

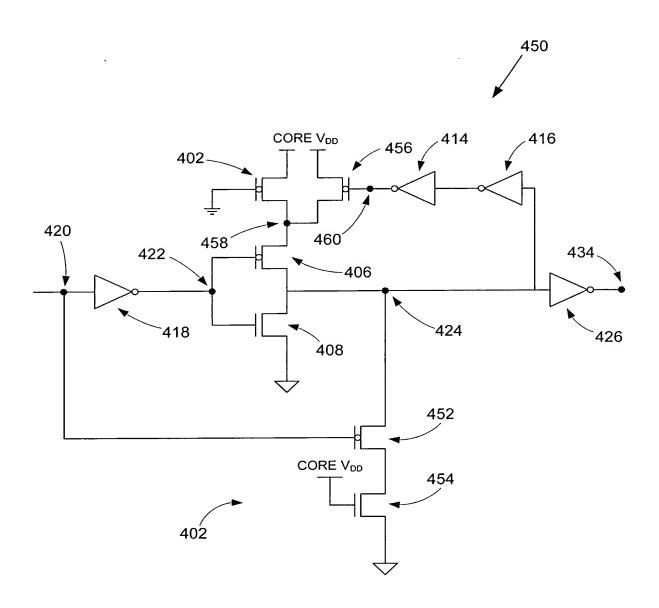


FIG. 4B

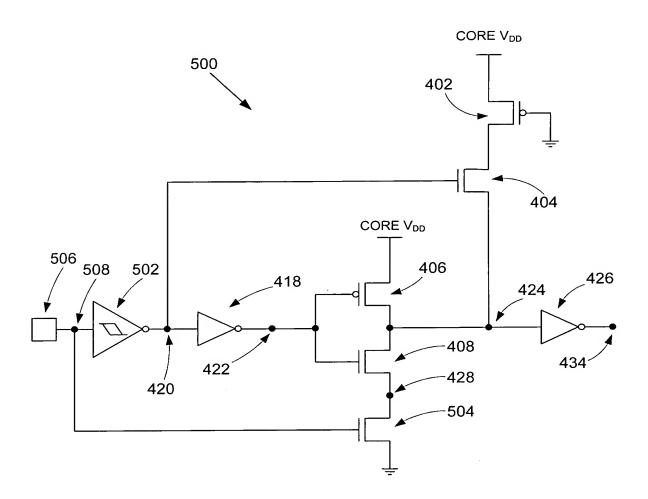


FIG. 5

REPLACEMENT SHEET

